

S/N 09/644463

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Matthew B. Haycock et al.	Examiner:	Raymond Phan
Serial No.:	09/644463	Group Art Unit:	2181
Filed:	August 23, 2000	Docket:	884.303US1
Title:	SIMULTANEOUS BIDIRECTIONAL PORT WITH SYNCHRONIZATION		
Assignee:	Intel Corporation	Customer No.:	21186

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have authorized the fee of \$180.00, as set forth in 37 C.F.R. §1.17(p), to be charged to Deposit Account No. 19-0743. Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

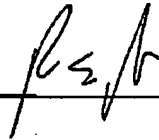
Respectfully submitted,

MATTHEW B. HAYCOCK ET AL.

By their Representatives,

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Date 27 AUGUST 2003

By 
Robert E. Mates
Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 27 day of August, 2003.

Name KACIA LEE

Signature 